

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant :	Masaharu Nagai et al.	Art Unit :	1795
Serial No. :	10/694,986	Examiner :	Daborah Chacko Davis
Filed :	October 29, 2003	Confirmation No.:	5334
		Notice of Allowance Date: June 21, 2010	
Title :	METHOD FOR REMOVING RESIST PATTERN AND METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE		

**MAIL STOP ISSUE FEE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

## RESPONSE TO NOTICE OF ALLOWANCE

In response to the Notice of Allowance mailed June 21, 2010, enclosed is a completed issue fee transmittal form PTOL 85b. The fees in the amount of \$1810 for the issue fee (\$1510) and the publication fee (\$300) are being paid concurrently herewith on the Electronic Filing System (EFS) by way of Deposit Account authorization.

Please apply any additional charges or credits to our Deposit Account No. 06-1050.

Respectfully submitted,

Date: September 20, 2010

  
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